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APPLICANTS
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**** CONTINUING DATA ******* *None TT*

**** FOREIGN APPLICATIONS ******* *Verified*
JAPAN 2002-320912 11/05/2002

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS <i>12</i> 15	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>TT</i> Initials			

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TITLE
Layout Design Method of Semiconductor Integrated Circuit, and Semiconductor Integrated Circuit, with High Integration Level of Multiple Level Metalization

FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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